The Oxford College of Science
Accredited by NAAC with A+grade in cycle III
Recognized by the Govt. of Karnataka; Permanently affiliated to Bangalore University & Approved by AICTE, New Delhi
Recognized by UGC under section 2(f) & 12(B); Recognized by GoK for BiSEP (formerly BTFS)
Supported by DST GoI under FIST program, Supported by DBT GoI under DBT-STAR College

DEPARTMENT OF ELECTRONICS

SL.NO	DETAILS OF THE EVENT	
1.	Title Of The Event	Workshop on "FPGA Interfacing"
2	Day and Date	Saturday, 31st May 2025
3	Venue	Electronics lab
4	Organizing Secretary/ Co-Coordinators	Ms Gayathri Sudhir
5	Participants	M.Sc First year Electronics Students
6	No of students and staff present	5 students, 2 staff

Resource Person: Mr Nikhil B Goranal

The Department of Electronics at The Oxford College organized **"Interfacing FPGA Using Xilinx"** workshop on 31st May 2025 at The Oxford College, Bengaluru. A two-hour technical workshop on "Interfacing FPGA using Xilinx" was conducted by Mr. Nikhil, aimed at providing a foundational understanding of Field Programmable Gate Arrays (FPGAs) and their interfacing techniques using the Xilinx toolchain. The session was designed to give participants a brief yet impactful exposure to digital design implementation.

Coordinator   **Head**   **Principal**